PATENT

19270 U.S.PTO 10/713492 11403

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Transmittal

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is the Patent Application of:

INVENTORS: Douglas J McKenney, Steven Mark Emerson
For: FLEXIBLE DESIGN FOR MEMORY OF INTEGRATED CIRCUITS
ASSIGNEE NAME: LSI LOGIC CORPORATION
ASSIGNEE RESIDENCE: MILPITAS, CALIFORNIA

Enclosed are:

☒ 37 Pages of Specification and 1 Abstract
☒ 13 Sheets of Drawings
☒ A Declaration and Power of Attorney
☐ An Information Disclosure Statement and form PTO-1449
☐ A certified copy of
☒ An assignment of the invention to LSI Logic Corporation, Milpitas, California.

The filing fee has been calculated as follows:

Other Than Small Entity

For:	No. Filed	No. Extra	Rate	Fee
Basic Fee				$ 770.00
Total Claims	20 - 20	= 0	x $18.00=	$ 00.00
Independent Claims	5 - 3	= 2	x $86.00=	$ 172.00
☐ Multiple Dependent Claim Presented			$270.00	$ .00
			TOTAL	$ 942.00

EXPRESS MAIL CERTIFICATE

Express Mail Label No.: EJ855161327US Date: **14 November 2003**
I hereby certify that I am depositing the enclosed or attached paper with the U.S. Postal Service "Express Mail Post Office to Addressee" service on the above date, addressed to the Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karuna Ojanen

Deposit Account Authorization:

☒ Please charge Deposit Account No. 12-2252 in the amount of $942.00. A duplicate copy of this sheet is enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 12-2252. A duplicate copy of this sheet is enclosed.

☒ Any additional filing fees required under 37 C.F.R. §1.16.

☒ Any patent application processing fees under 37 C.F.R. §1.17.

Respectfully submitted,

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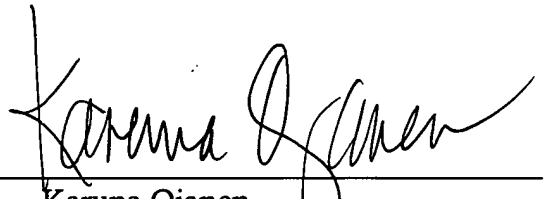
PATENT TRADEMARK OFFICE

By 

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Date: 14 November 2003